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(FILE 'USPAT' ENTERED AT 08:28:19 ON 26 APR 95)

L1 6 S COLLABORATIVE EDIT?
L2 1 S DISTRIBUT? EDIT?
L3 29573 S REALTIME OR REAL TIME
L4 94902 S CONCURRENT? OR CONTEMPOR?
L5 365996 S SAME TIME
L6 10 S WYSIWIS
L7 0 S COOPERATIVE EDIT?
L8 486308 S SIMULTANEOUS?
L9 3 S (L1 OR L2) (P) (L3 OR L4 OR L5 OR L6 OR L8)
L10 10 S COLLABORATIVE (P) (L3 OR L4 OR L5 OR L8) (P) VIEW?
L11 2 S STEFIK?/IN
L12 79 S TATAR?/IN
L13 1 S TATAR, D?/IN
L14 6 S KAHN, K?/IN
L15 3 S LANNING, S?/IN
L16 2 S KASPERSKI?/IN
L17 6 S CANTATA
L18 67 S SARIN?/IN
L19 0 S SARIN?/IN AND CONFERENC?
L20 1 S SARIN, S?/IN
L21 32 S INTERACTIVE (3W) CONFERENC?
L22 7 S L21 AND EDIT?

=> d l21 2 ti pd fd parn ccls kwic

US PAT NO: 5,300,943 [IMAGE AVAILABLE] L21: 2 of 32
TITLE: Multiple display workstation with conductive surface
overlay control
DATE ISSUED: Apr. 5, 1994
DATE FILED: Aug. 21, 1991

PARENT-CASE:

This application is a continuation of application Ser. No. 07/395,160,
filed Aug. 17, 1989, now abandoned, which is a continuation of Ser. No.
06/914,924, filed Oct. 3, 1986, now abandoned.
US-CL-CURRENT: 345/1, 173

SUMMARY:

BSUM(39)

The . . . real-time voice, data, and video communications allow the
user extensive system flexibility. Two or more systems can link to allow
fully-****interactive**** real time distributed ****conferencing**** and editing
wherein dispersed users can work cooperatively on images viewed by all of
them. For example, users at various. . .

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364/200, 234, 234.3, 236.1, 237.2, 237.3, 238.2, 238.3, 238.4, 238.6, 238.8, 239, 239.1, 241.2, 241.3, 241.6, 242.1, 242.6, 242.7, 243, 243.4, 246, 254, 254.5, 260, 260.1, 262.4, 263, 264, 264.5, 264.6, 265.1, 266.3, 266.4, 266.5, 267, 267.4, 267.5, 267.6, 267.7; [IMAGE AVAILABLE]

370/110.1, Apr. 16, 1985, Digital dual channel communication terminal; et al., 370/110.1

U.S. Patent & Trademark Office P0023

370/110.1, Apr. 16, 1985, Digital communication station signaling; et al., J. Fulcomer, Jr., et al., 370/110.1

364/476; 33/706; 72/6, 7, 8, 36, 380, 825.04, 825.05, 825.5; 364/142, 174, 474.07, 474.19, 603;

364/900, 926.5, 926.9, 927.8, 927.92, 927.93, 927.96, 932.8, 933.9, 935, 936.1, 939, 939.2, 940, 940.1, 940.2, 940.4, 940.81, 942, 948.1, 977.5 [IMAGE AVAILABLE]

370/110.1, Feb. 28, 1984, Self-switched data port in-band signaling; et al., Robert P. Barner, Jr., et al., 370/110.1

U.S. Patent & Trademark Office P0024

370/84, Feb. 27, 1973, VOICE OR ANALOG COMMUNICATION SYSTEM EMPLOYING ENCODING TECHNIQUES; Carl Newton Abramson, et al., 370/84

370/110.1, Jul. 4, 1989, Dynamic buffer supervising system for a data protocol control; Tamiya Nakamura, et al., 370/110.1, 58.1

370/110.1, Jul. 4, 1989, Dynamic buffer supervising system for a data protocol control; Tamiya Nakamura, et al., 370/110.1, 58.1

364/200; 340/825.5; 364/222.2, 228.3, 229, 229.2, 240, 244, 244.3; 370/85.1; 371/12, 68.2; 375/36 [IMAGE AVAILABLE]
U.S. Patent & Trademark Office P0025

364/900, 919, 920, 926.1, 926.9, 927.2, 929, 929.1, 932, 932.62, 942, 942.06, 942.3, 942.4, 943.9, 944.5, 945.8, 947, 950, [IMAGE AVAILABLE]

364/900, 919, 920, 921.4, 921.8, 921.9, 926, 926.1, 926.3, 929, 929.1, 934, 934.1, 939, 939.2, 940, 942, 942.06, 942.3, 942.4, 945.9, 946.3, 949.5 [IMAGE AVAILABLE]

5C34

3,473,117, May 7, 1989, Distributed routing unit for fully-automated
routing system; Roger D. Burney, 364/478; 198/349; 235/376;
[IMAGE AVAILABLE]

U.S. Patent & Trademark Office

P0019

3,473,120, May 20, 1989, Subscriber RF telephone system for providing
voice and/or data signals simultaneously over either a single or a
multiple channels; Eric Paneth, et al., 370/24, 95.1, 109

3,473,121, May 7, 1989, Communications network; Remo G. A. Marzolini,
[IMAGE AVAILABLE]

3,473,122, Jul 26, 1989, Remote I/O port for transfer of I/O data in a
network; William E. Floro, et al., 364/900, 919, 926.1,
927.2, 927.3, 931.4, 931.44, 933.8, 933.9, 939, 939.2, 948.1, 949, 950, 950.1
[IMAGE AVAILABLE]

3,473,123, Apr 25, 1988, Real time data reduction system standard
application; Donald D. Conklin, et al., 364/900; 342/195; 364/922.5,
927.2, 927.3, 931.4, 931.44, 933.8, 933.9, 939, 939.5, 944.9,
[IMAGE AVAILABLE]

U.S. Patent & Trademark Office

P0020

3,473,124, Apr 19, 1988, Decentralized line reservation interface
for a network; Michael S. Friedman, et al., 340/825.5; 370/94.1

3,473,125, Feb 9, 1988, Method for operating a local terminal to
execute an application program; James M. Carron, et al., 364/900,
379/91, 96; 902/24, 37 [IMAGE AVAILABLE]

3,473,126, Feb 9, 1988, Modular multiport data hub; Peter M. Athanas,
[IMAGE AVAILABLE]

3,473,127, Jan 23, 1987, Subscriber RF telephone system for providing
voice and/or data signals simultaneously over either a single or a
multiple channels; Eric Paneth, et al., 370/50, 109; 379/59; 455/33

3,473,128, Jun 9, 1987, Network interface module and method; Richard P.
[IMAGE AVAILABLE]
[IMAGE AVAILABLE]
U.S. Patent & Trademark Office

P0021

3,473,129, May 12, 1987, Wireless computer modem; Tommy L. Kirchner, et
al., 375/8, 95.3; 375/8

3,473,130, Jan 27, 1987, Method and apparatus for performing work in a
distributed space; Robert L. Anderson, et al., 318/599, 568.1, 570,
474.12, 474.22, 474.23, 474.34 [IMAGE AVAILABLE]

3,473,131, May 25, 1986, All digital IDMA dynamic channel allocated
communications system and method; Kap S. Kim, et al., 370/95.3

3,473,132, Jun 24, 1986, Transceiver for multi-drop local area
network; [IMAGE AVAILABLE], et al., 371/32; 375/8, 92, 99

3,473,133, May 26, 1986, Token access controller protocol and
apparatus; Mark Cieglikta, 340/825.5, 825.52; 364/200, 222.2, 230, 230.3,
230.4, 230.5, 230.6, 230.7, 230.8, 230.9, 230.10, 230.11, 230.12, 230.13,
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243.04, 243.05, 243.06, 243.07, 243.08, 243.09, 243.10, 243.11,

1984, MEMORY ACCESS DEVICE; Eugene Francis Dumstorff,
927.2, 927.4, 929.2, 933, 933.2, 933.3, 937, 939,
942, 946.6, 948.1, 948.2, 950, 950.2, 952, 952.1,
965, 966.6, 966.1, 966.3, 966.4 [IMAGE AVAILABLE]

1983, Flow control mechanism for block switching
et al., 370/60

1983, Flow control mechanism for block switching
et al., 370/60

U.S. Patent & Trademark Office

P0016

1980, Push-pull serial bus coupled to a plurality of
collision detection circuit and arbitration circuit; Sean
340/825.5; 364/222.2, 228.3, 229, 229.2, 240,
370/85.1; 371/12, 68.2; 375/36 [IMAGE AVAILABLE]

1980, Method and apparatus for interfacing a system
processor; R. Stephen Polzin, et al., 370/94.1, 85.9

1978, Improved pointer FIFO controller for converting
distributed dual FIFO by controlling the RAM's address
et al., 364/900, 926.1, 926.3, 926.9, 927.81, 955,
[IMAGE AVAILABLE]

U.S. Patent & Trademark Office

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